

FIG. 1

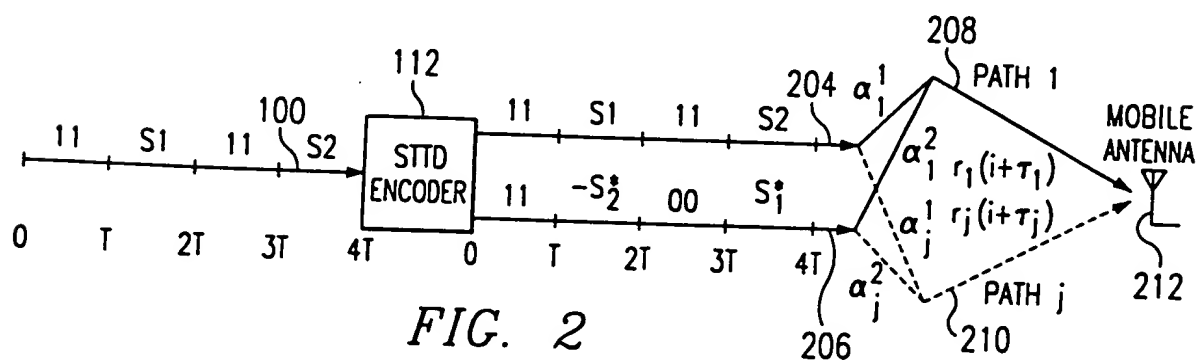


FIG. 2

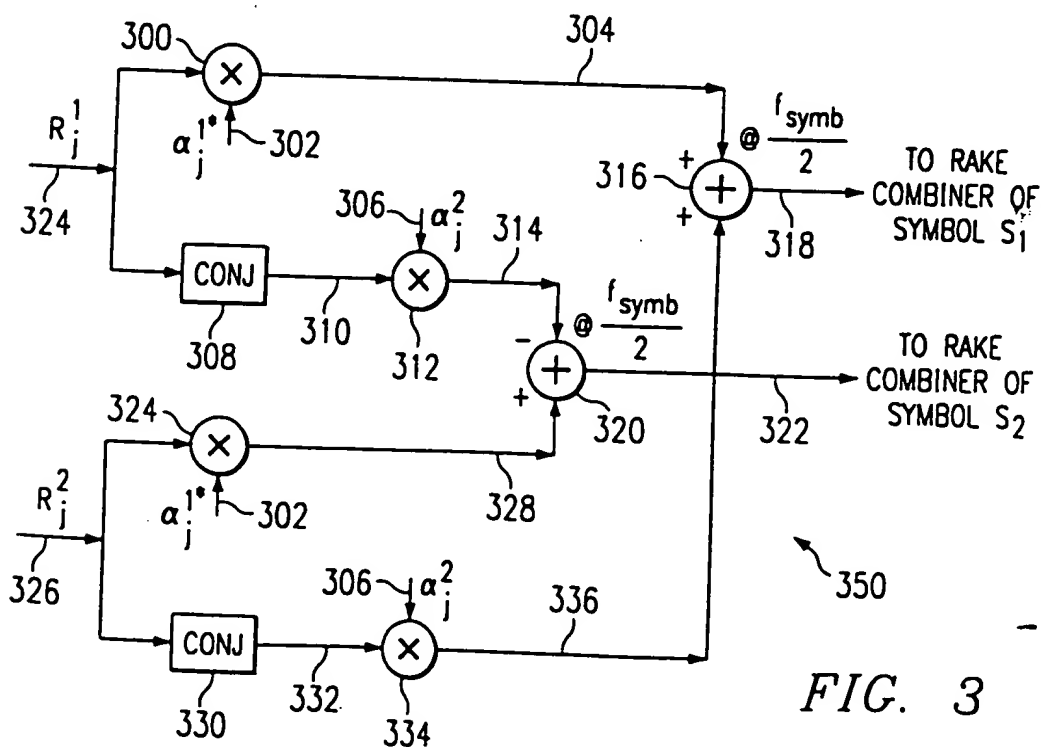
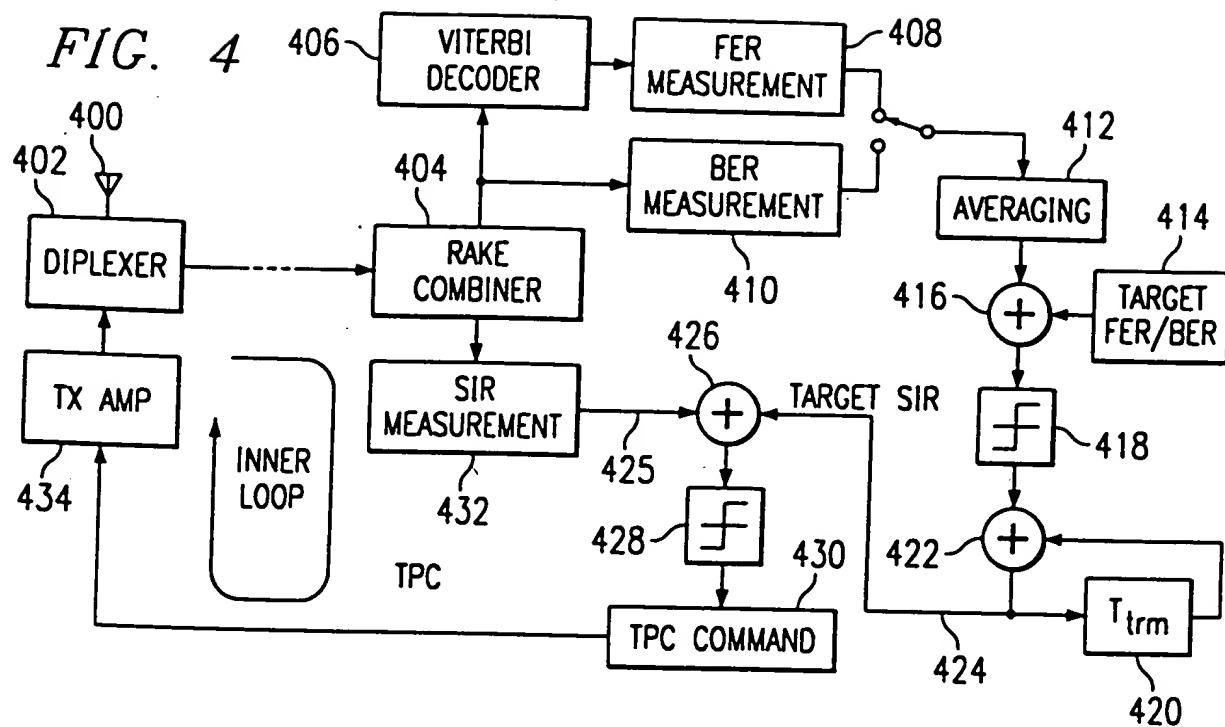


FIG. 3



The diagram illustrates a mobile communication system architecture. At the top center is a rectangular box labeled "MOBILE UNIT" with reference numeral 512. Below it are three Base Transceiver Stations (BTS1, BTS2, and BTS3), each represented by a triangle with an antenna on top. At the bottom center is a rectangular box labeled "NETWORK CONTROL" with reference numeral 500. Radio links, indicated by wavy lines, connect the Mobile Unit to each BTS: 510 to BTS1, 514 to BTS2, and 516 to BTS3. Landline connections, indicated by solid lines, connect each BTS to the Network Control unit: 520 from BTS1, 521 from BTS2, and 523 from BTS3. Additionally, BTS1 is connected to two other antennas (509 and 508) via lines 502 and 504, respectively. These two antennas are also connected to the Mobile Unit via radio links 509 and 508.

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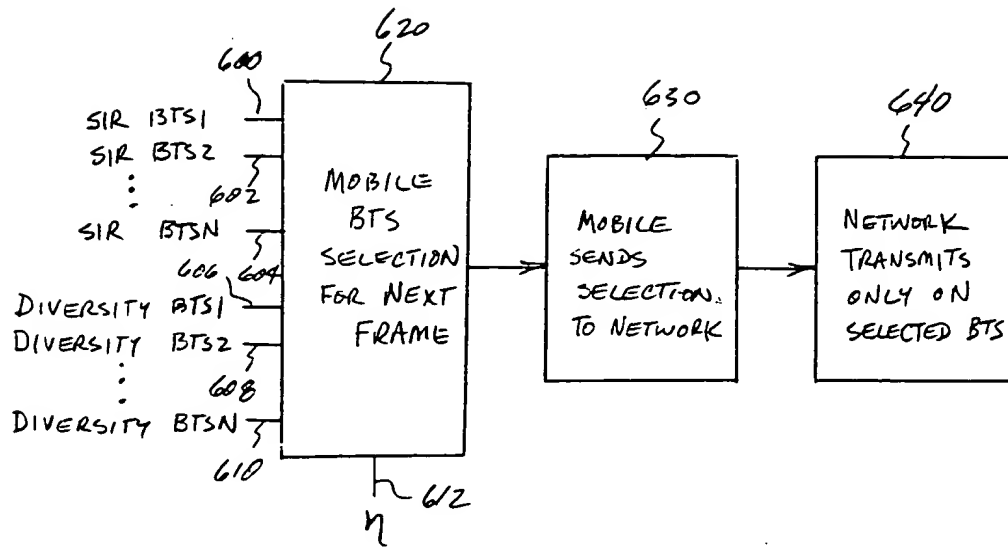


FIG. 6

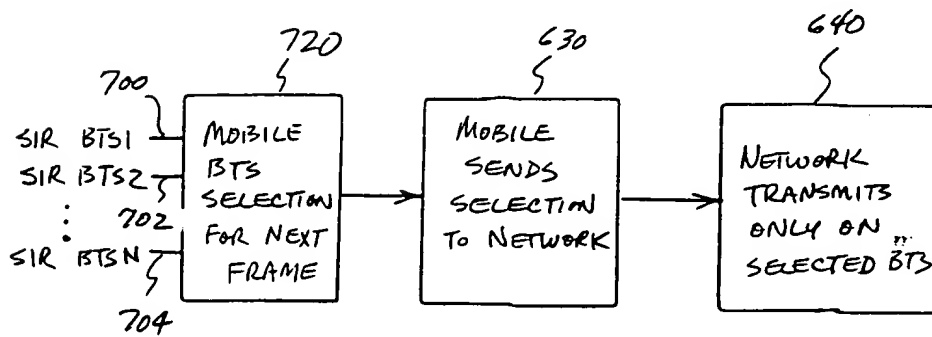


FIG. 7
(PRIOR ART)

Simulation environment	Indoor-to-outdoor pedestrian
Velocity	3 kmph
Data rate	32 KSPS
Information bit rate	8 Kb/s
Info/CRC/tail bit per frame	80/16/8
Convolutional coding rate	1/3, k = 9
Repetition	8 bits/10 ms (312 -> 320)
Interleaver	10 ms, 16*20
DCCH bits/frame	96
Pilot/TPC/TFI bits per slot	8/2/0
Channel Estimation	Perfect
Number of base stations in soft handoff	3
Simulation condition: only SSDT	No antenna diversity at any of the 3 base stations
Simulation condition: STTD + SSDT	Two transmit antennas with STTD encoding at each of the 3 base stations
Relative average powers of soft handoff base stations at the mobile	equal
Base station selection at mobile	Only SSDT : Based upon maximum received power STTD+SSDT: Based upon maximum received power
Frequency of base station selection for SSDT	Once per frame (10 msec.)
Up link errors in base station selection for SSDT (FBI bit error rate)	None
Forward link power control	Only SSDT: One slot averaging for the selected base station STTD+SSDT: One slot averaging for the selected base station
Power control step (dB)	1
Power control rate (Hz)	1600
TPC update delay (slots)	1
TPC error rate	5%
Desired coded BER	10^{-3}

FIG. 6

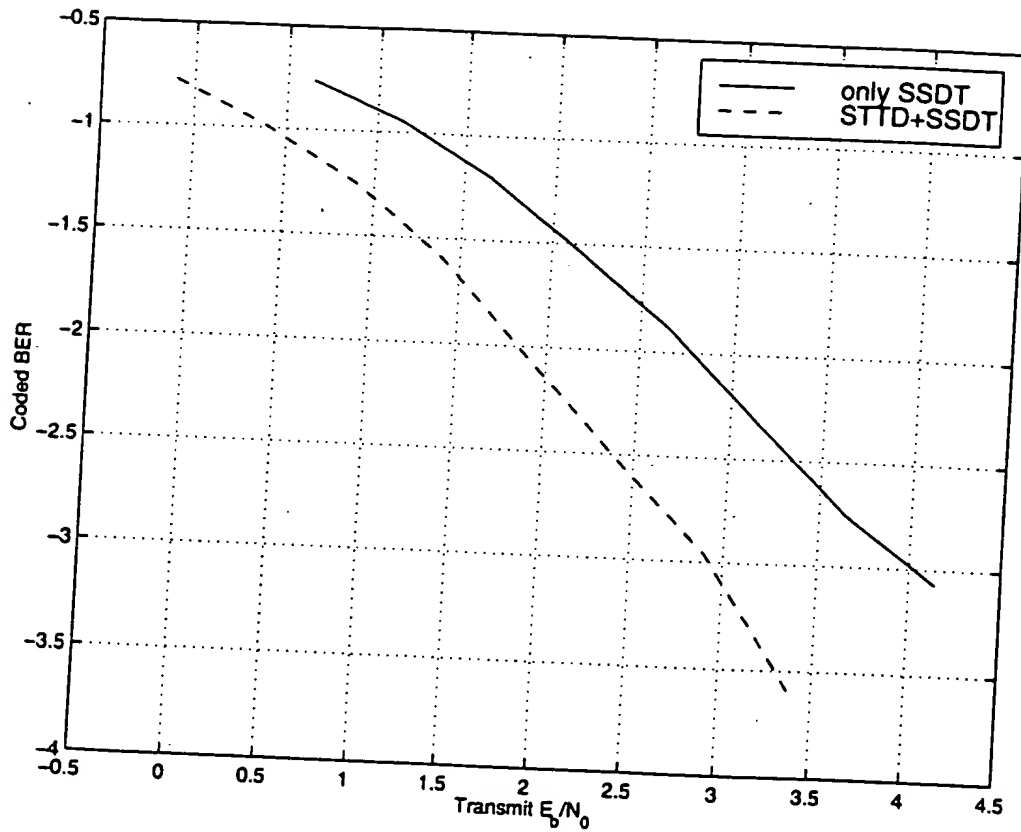


FIG. 9